

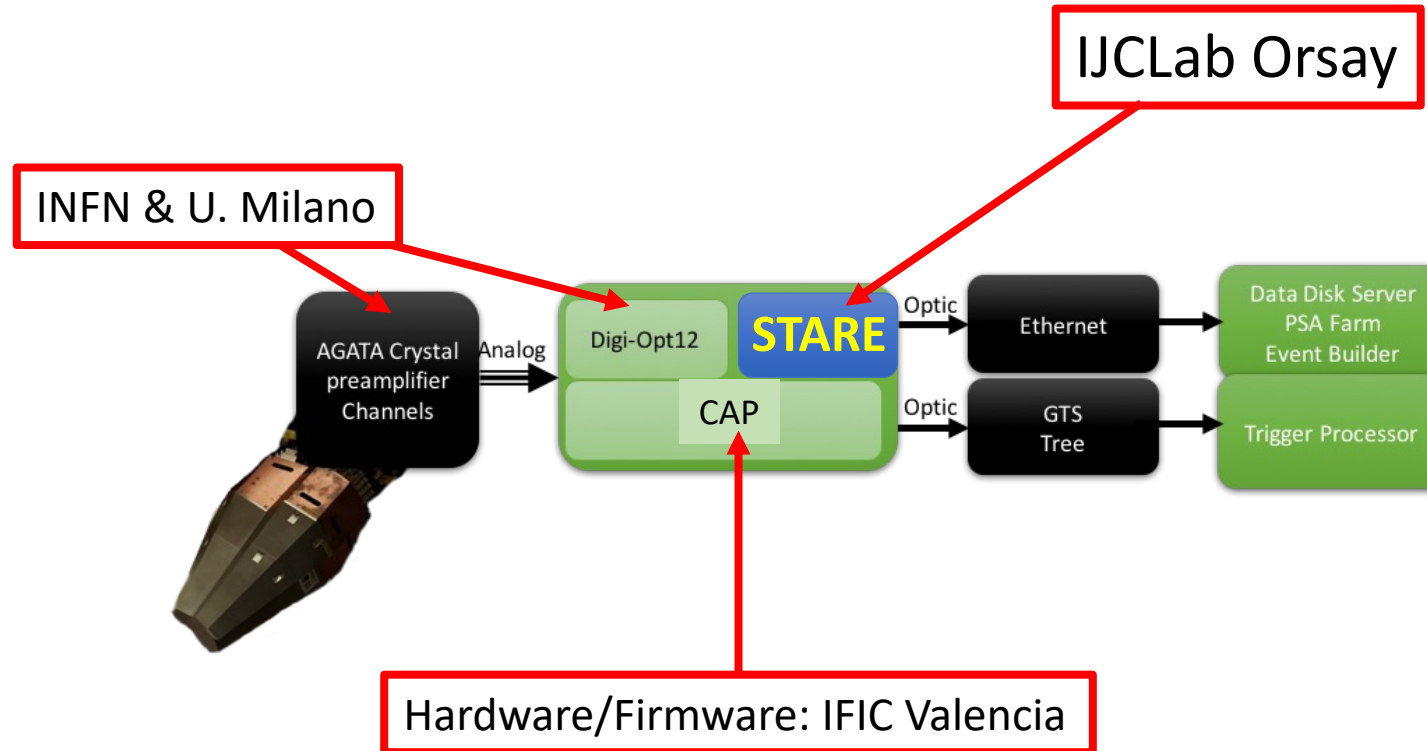
AGATA Project

STARE (Serial Transfer And Readout over Ethernet) and EMC @LNL

N. Karkour et al.

- ✓ STARE Production Status
- ✓ Production Tests Bench :
- ✓ 12 Links Crate for Readout
- ✓ EMC @ LNL

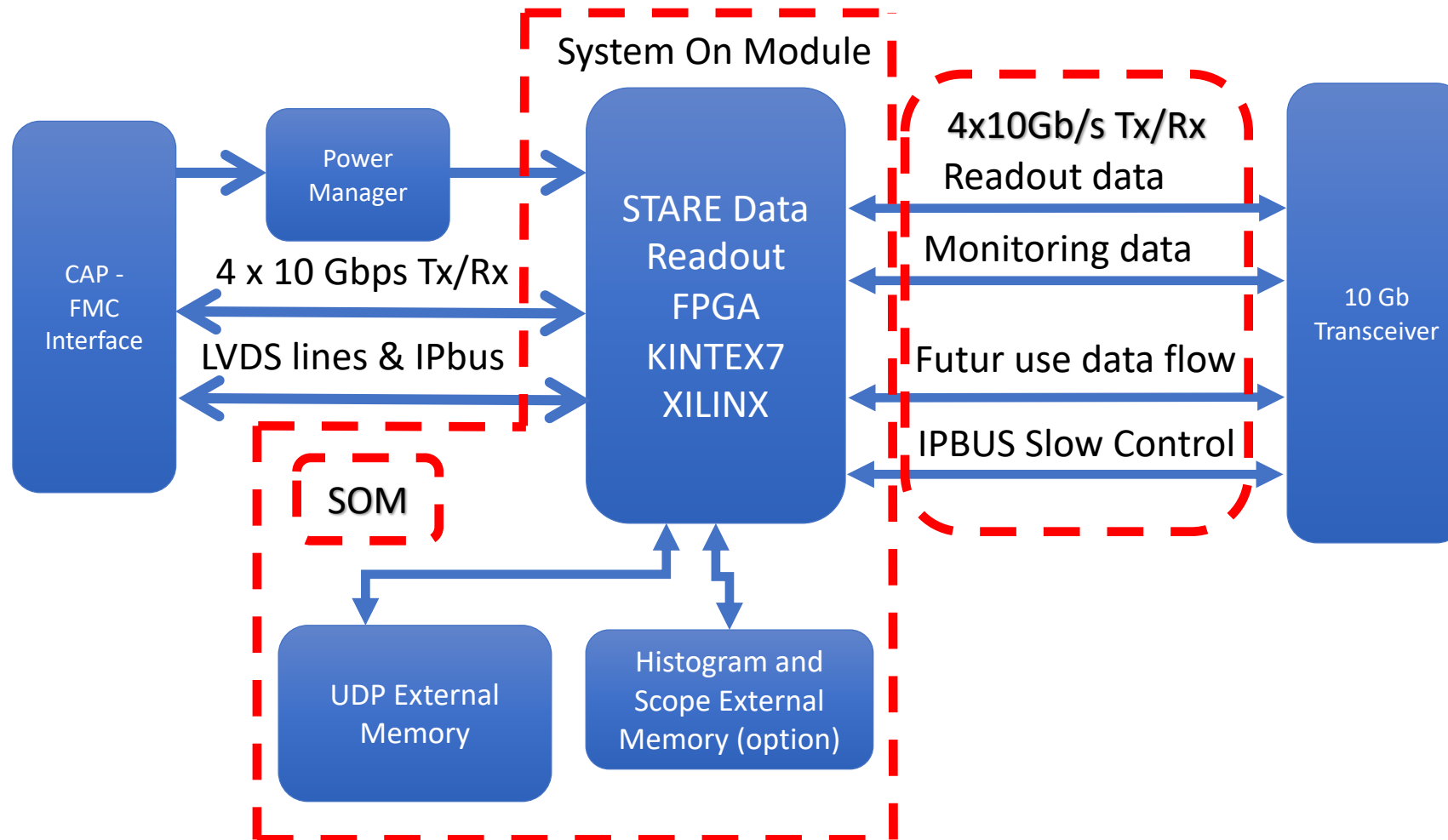
Hardware General Layout

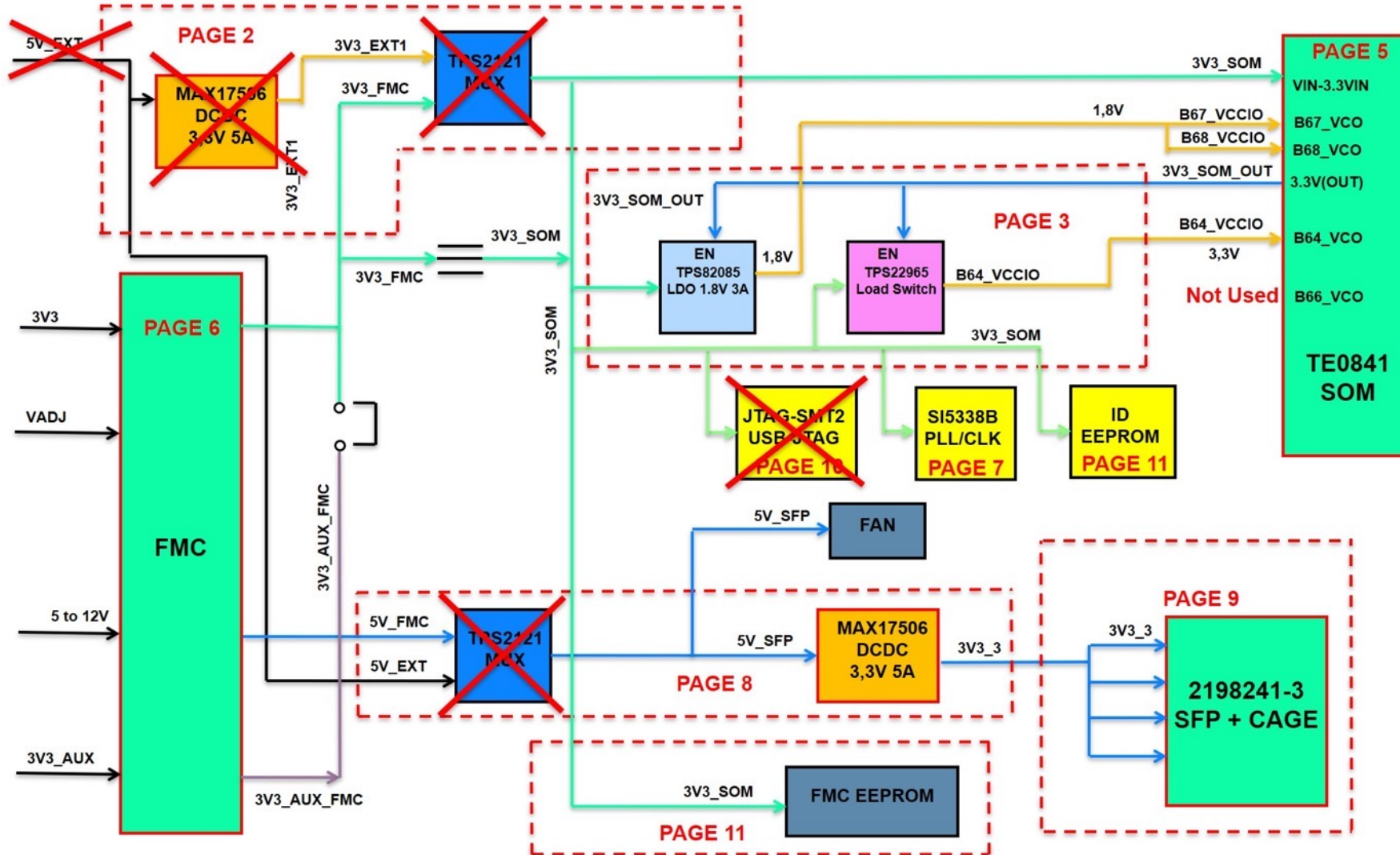


STARE characteristics

- Input data rate from pre-processing : 4 x 10 Gbps (2 for data, 1 for monitoring & 1 spare).
- 2 Power supply sources (Internal and external PSU using hot swap mux)
- Spare LVDS I/O with the pre-processing for future use
- Full clock management inside the SOM and the FMC carrier.
- Network Bandwidth up to 4 x 10 Gbps in parallel.
- 1 Gbps IPBus for slow control.
- Some on board facilities to make local comparisons with the DAQ (raw data storage, local Histograms etc...).
- With this configuration it is absolutely easy to achieve continuous and comfortable 10 Gbps with monitoring and online analysis using multiple 10 Gbps Tx/Rx.

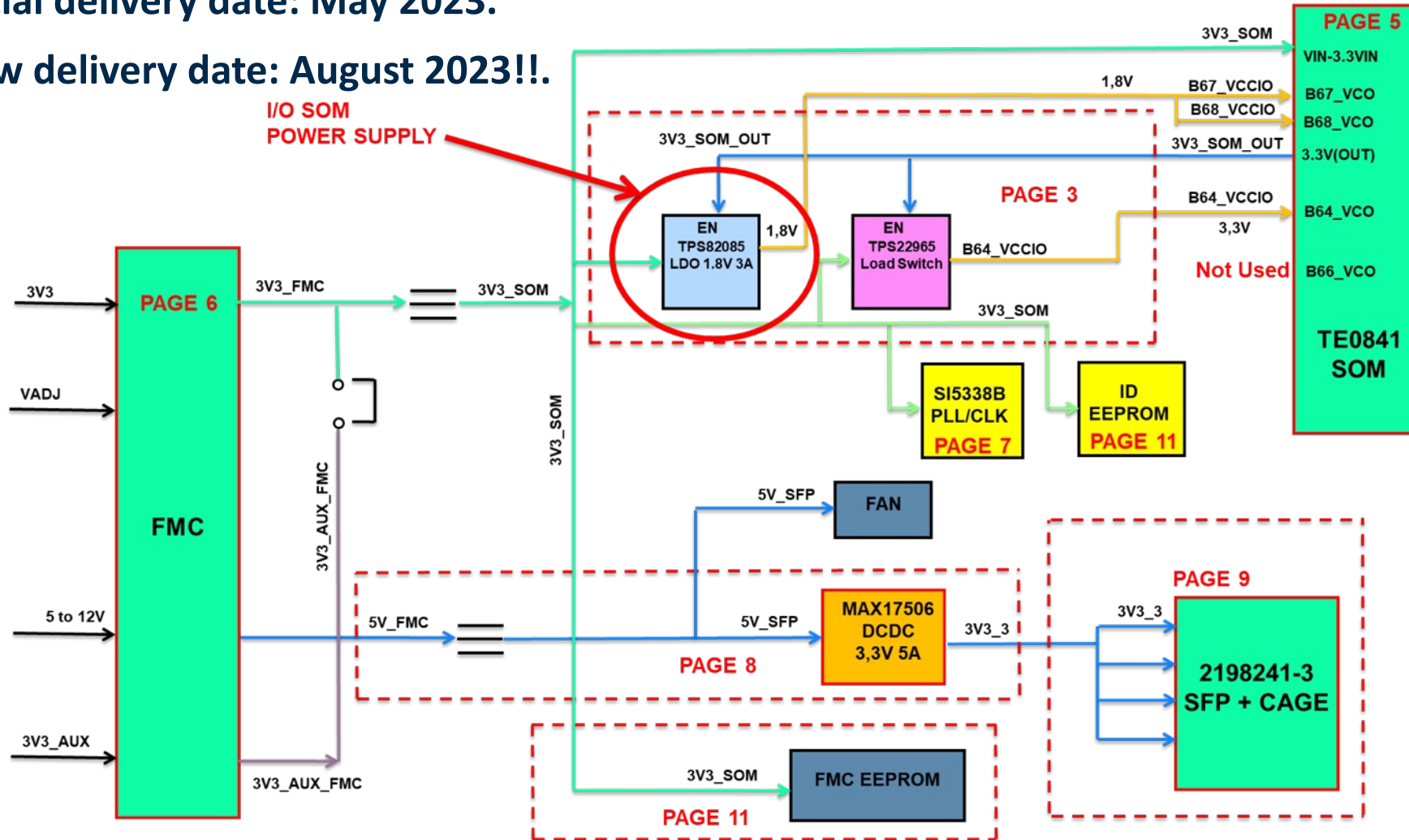
STARE characteristics





Initial delivery date: May 2023.

New delivery date: August 2023!!.



- New I/O SOM Power supply designed, PCB modified & ready to manufacture.
- Difficulties in purchasing Components: All components in stock.
- Ready to produce Pre-production (10 STARE V2): Delivery : Jan 2023.
- Production Feb 2023 to April 2023. Delivery: May 2023.

Top side



Bottom side



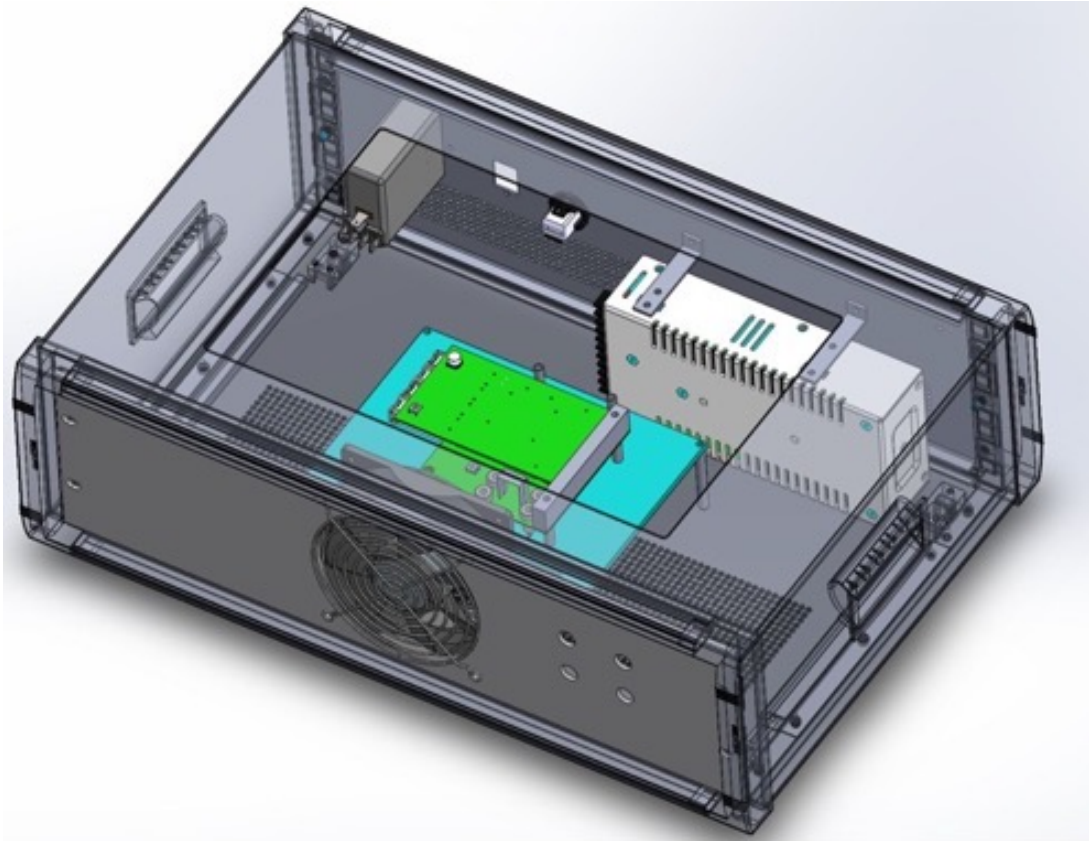
Artist View

- Final Year Project for Mathieu
- Dedicated for manufacturer : STARE V2 Production tests before delivery and also for the maintenance in the future.
- Covers more than 95 % of the components soldering reliability. All STARE interconnections will be tested.
 - EEPROM, Clock manager, Optical fiber, LVDS, Aurora, IPbus, Power Supply
- 2 Testbenches to be produced : 1 in the lab and 1 for the maintenance and production test @ the manufacturer.
- 1 is ready and functional, 1 is in production.

From mechanical design



Real Test bench



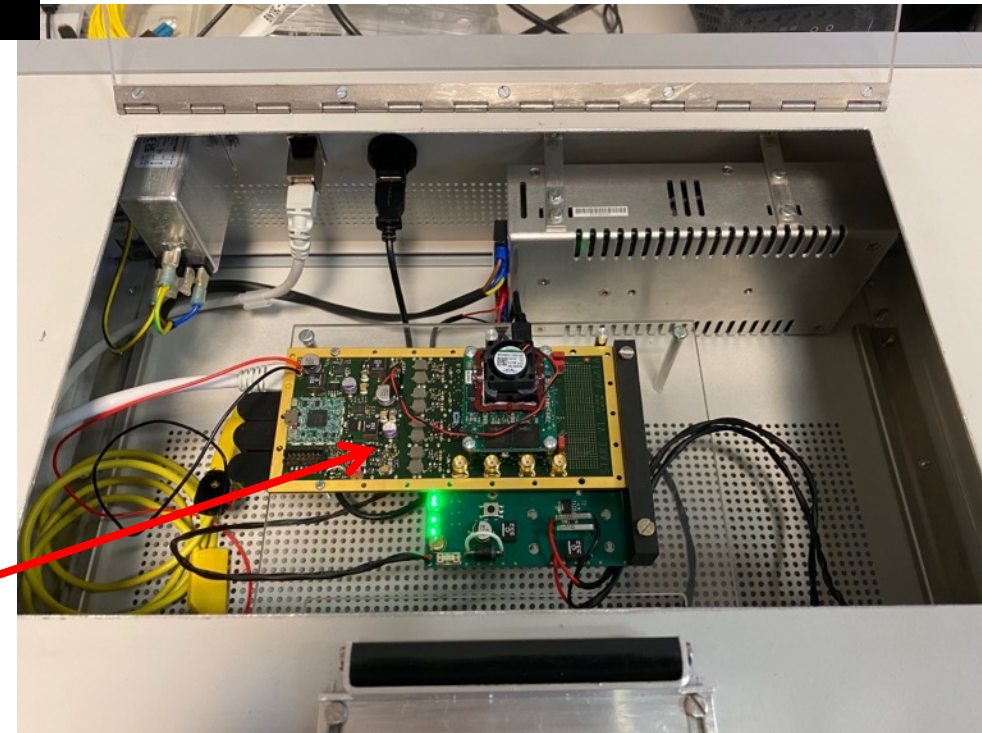
```
(Cmd) start_test_clock  
clock0 : 156252409  
clock1 : 156252409  
clock2 : 156252408  
clock3 : 156252410  
clock4 : 600008597  
clock5 : 156252238  
clock6 : 156252238  
clock7 : 600008592  
clock8 : 99999998
```

```
(Cmd) start_test_loopback  
error: 0  
loopback_out: 1347393  
loopback_in: 7897536  
data_check: 561300  
(Cmd) start_test_loopback  
error: 0  
loopback_out: 3710645  
loopback_in: 27373  
data_check: 8312302
```

```
(Cmd) start_test_eeprom 0  
write eeprom in progress for 51 sec ...  
address: 255  
data: 255  
test_eeprom OK
```

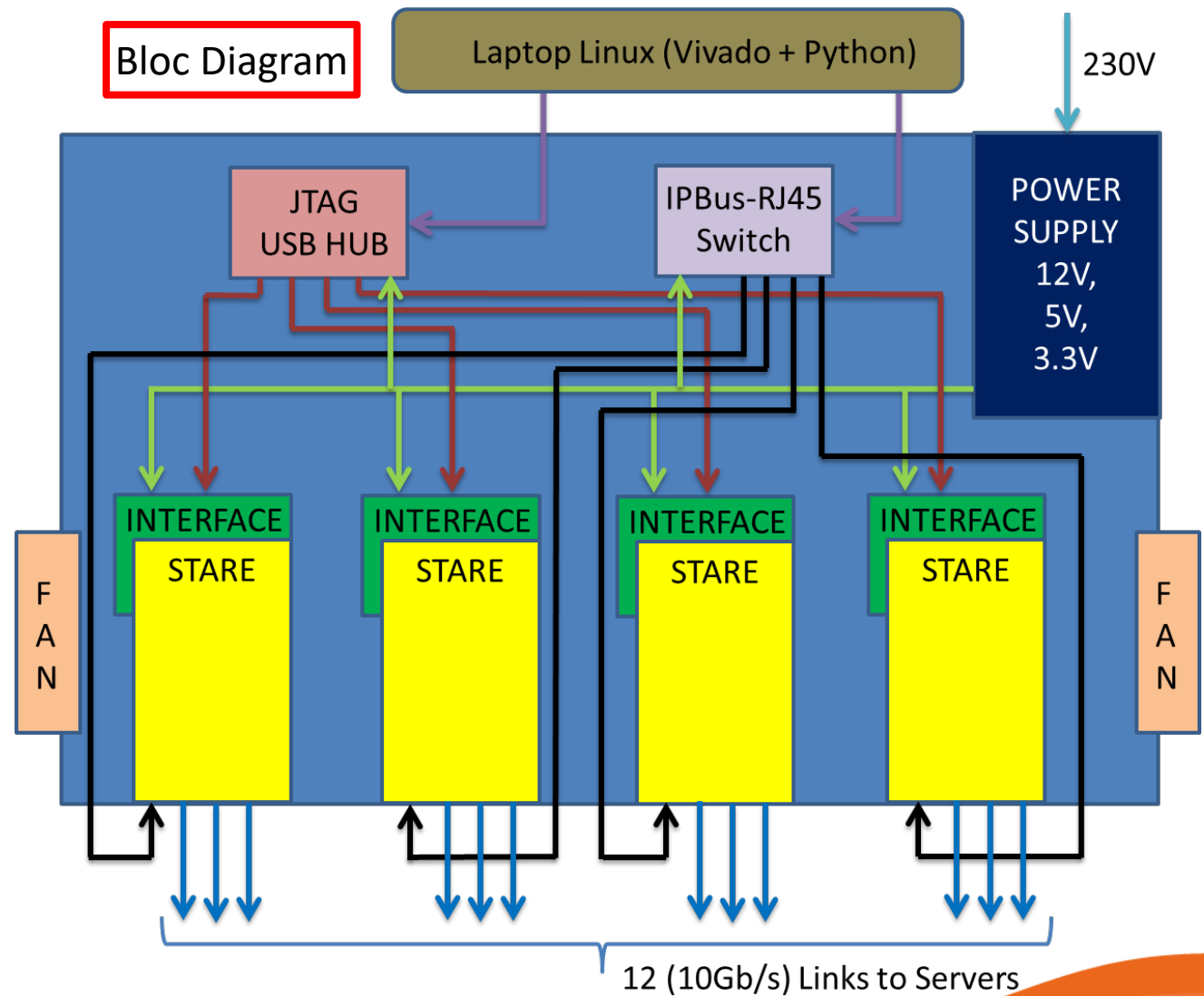


Front panel

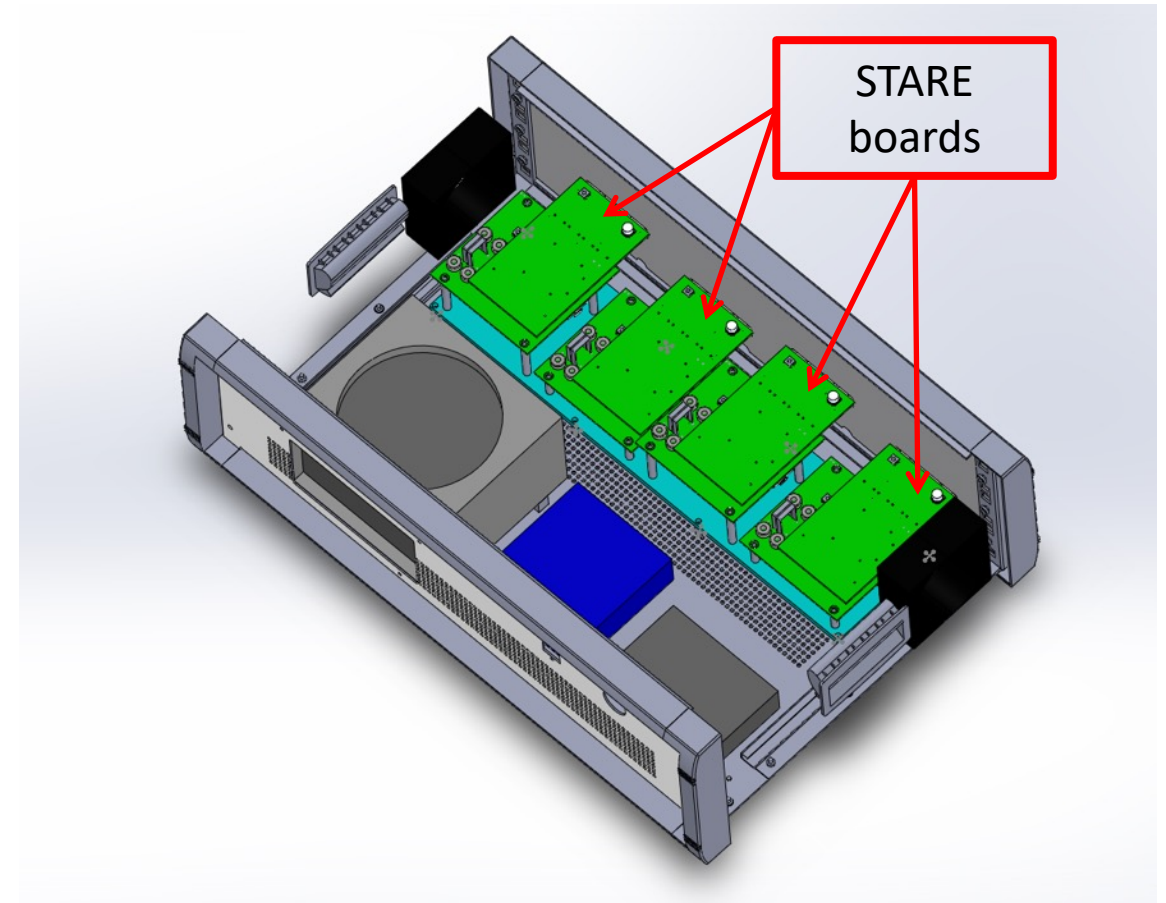
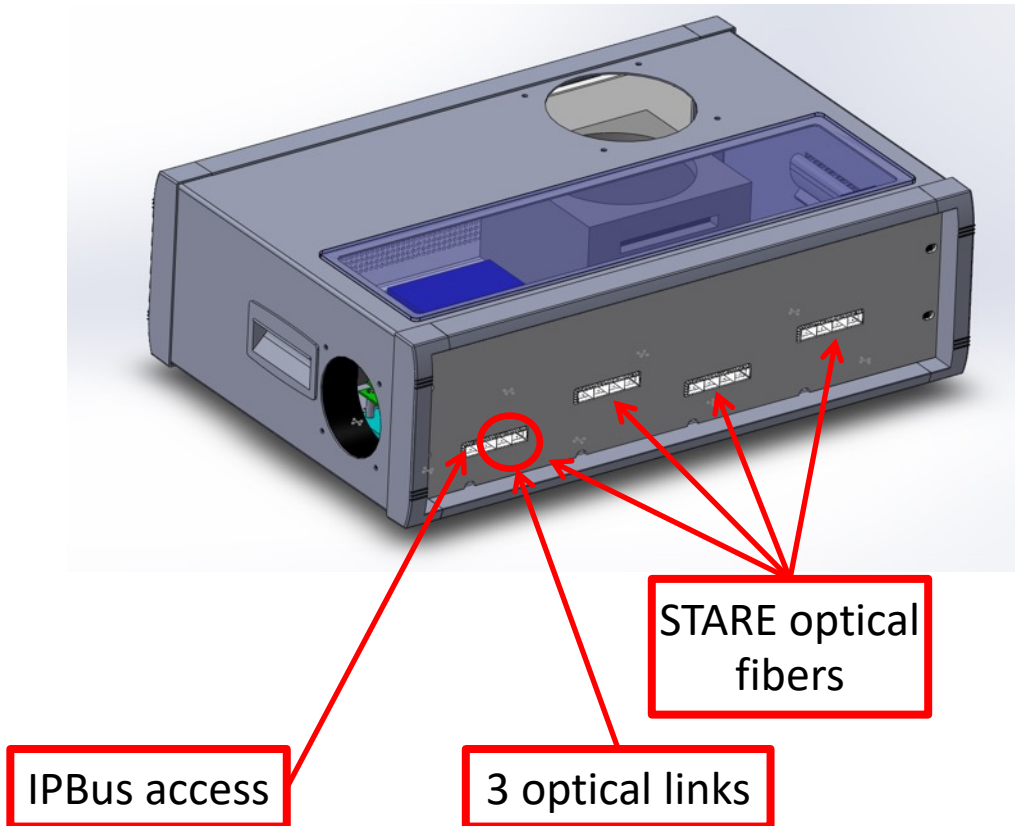


STARE under tests

- Designed to simulate actual and future 10 Ge crystals transferring up to 10 Gbps each or 4 Ge crystals transferring up to 30 Gbps each.
- 4 STARE V2 running: 12 optical links available (10 Gb/s each) for tests servers capabilities and server configurations for 10 Gbps receivers.
- RUDP tests on multiple crystals. Check for Packets loss statistics and qualify RUDP utility for the project.
- Back pressure program tests and qualification.
- High transfer rate demonstrator
- JTAG control for programming FPGAs.
- IPBus access for slow control actions.

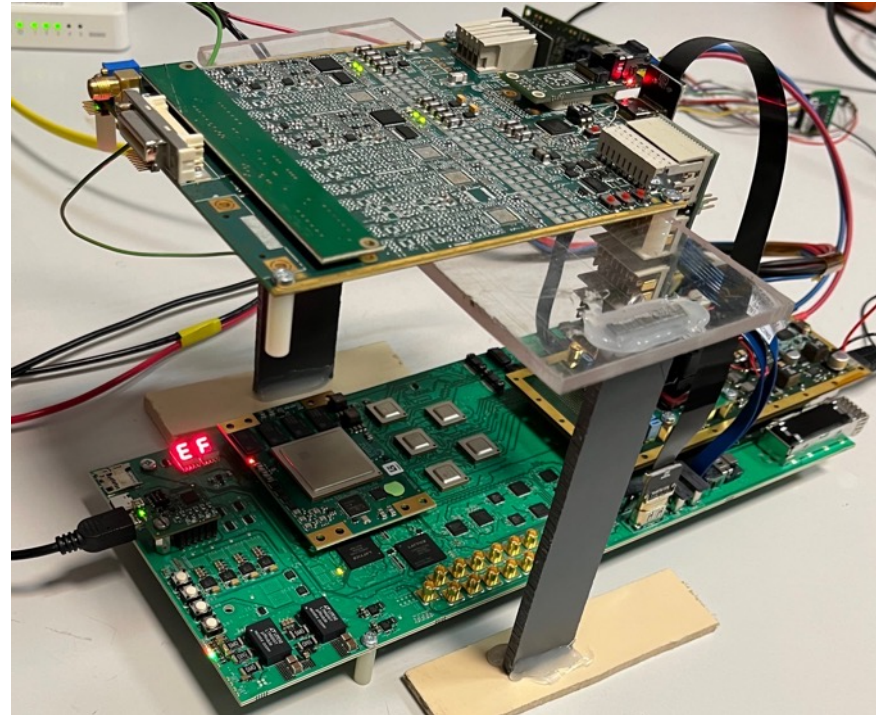


- CAD views : ready to manufacture, delivery Feb 2023



3 Test benches in Orsay :

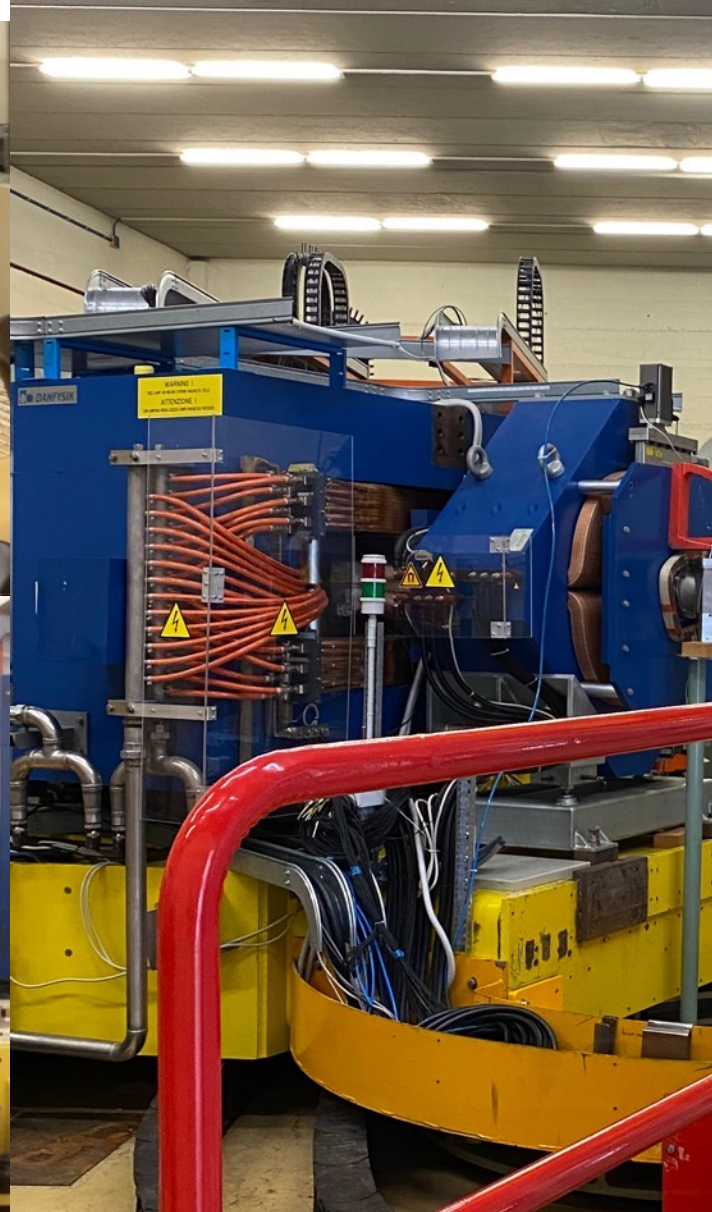
- 1 standalone in 102 building for Monique
- 1 standalone in AP1 104 building
- 1 with CAP board in AP1 104



CEM @ LNL 2021 and 2022

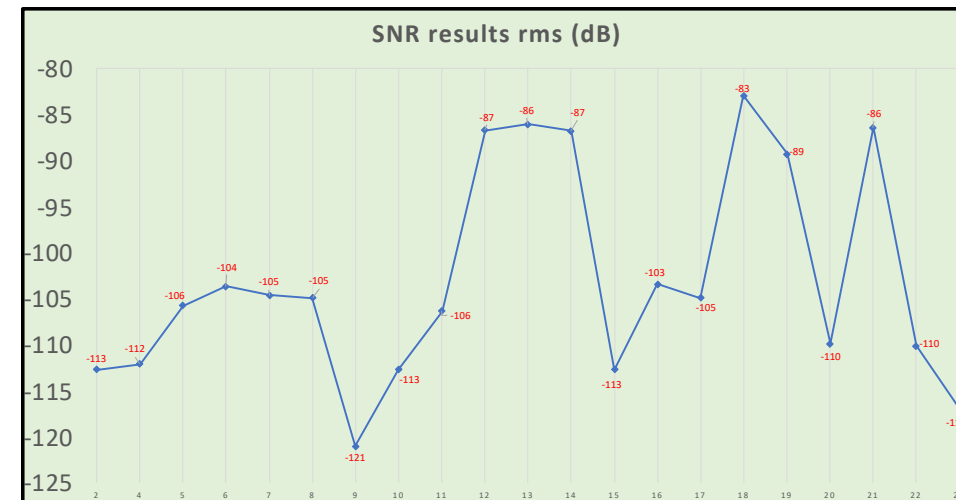
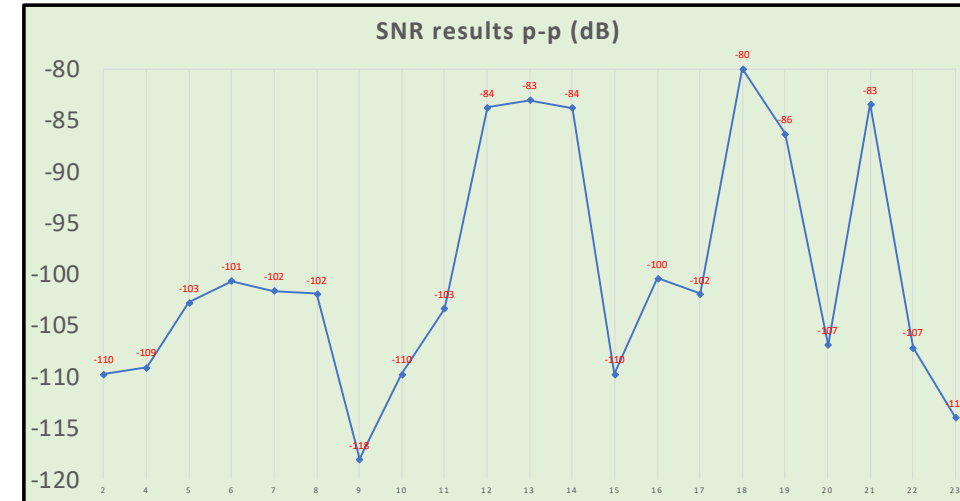
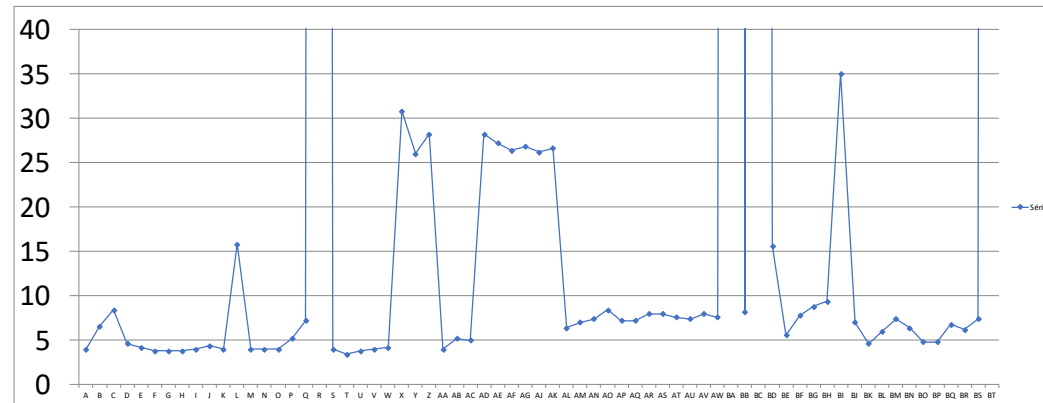
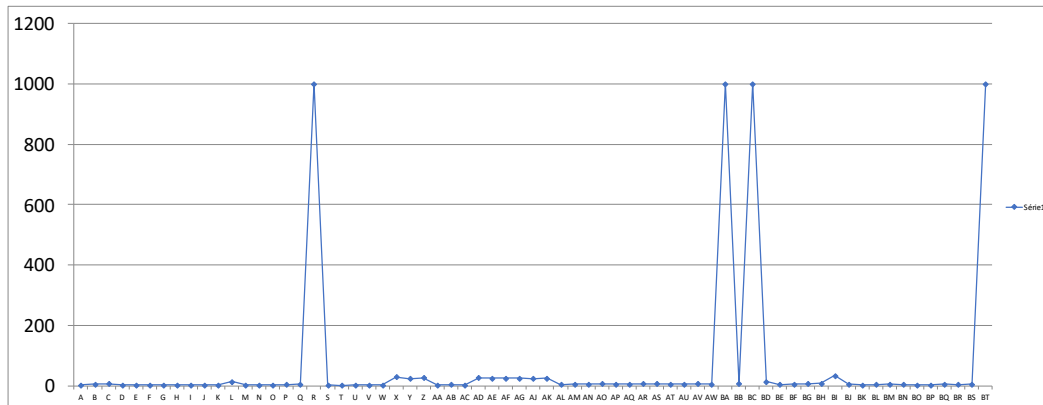
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AGATA Week 2021

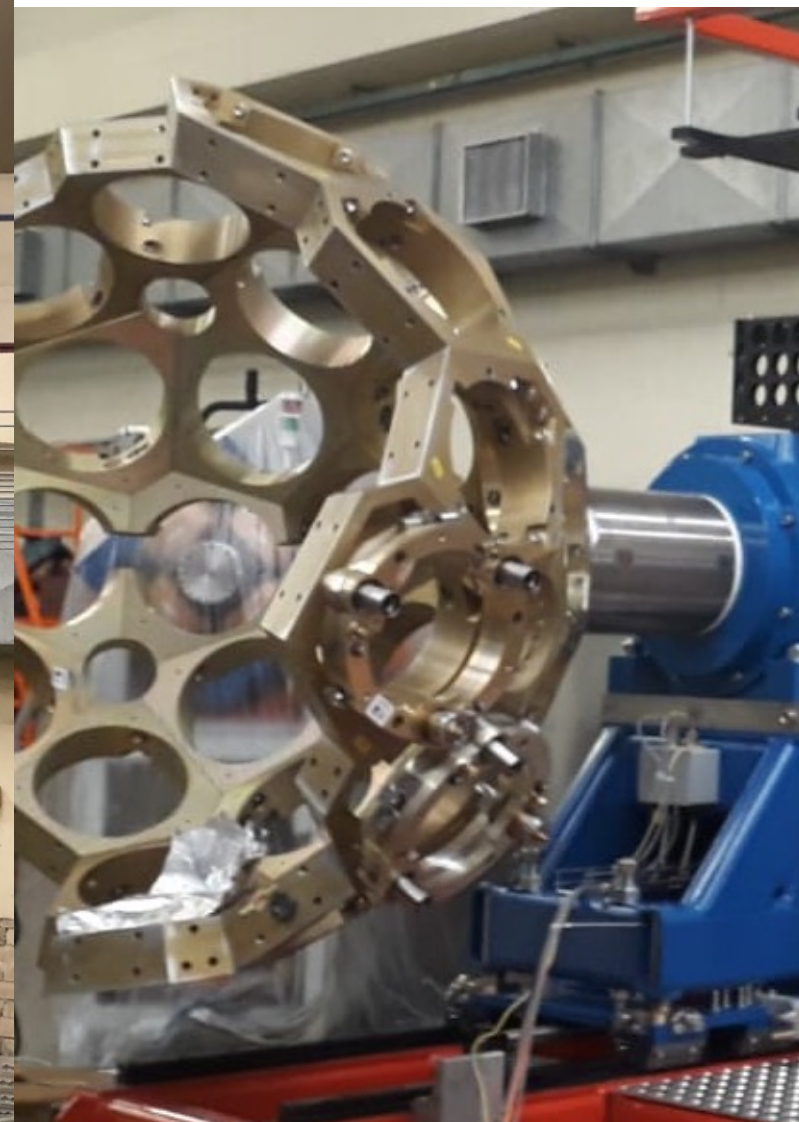
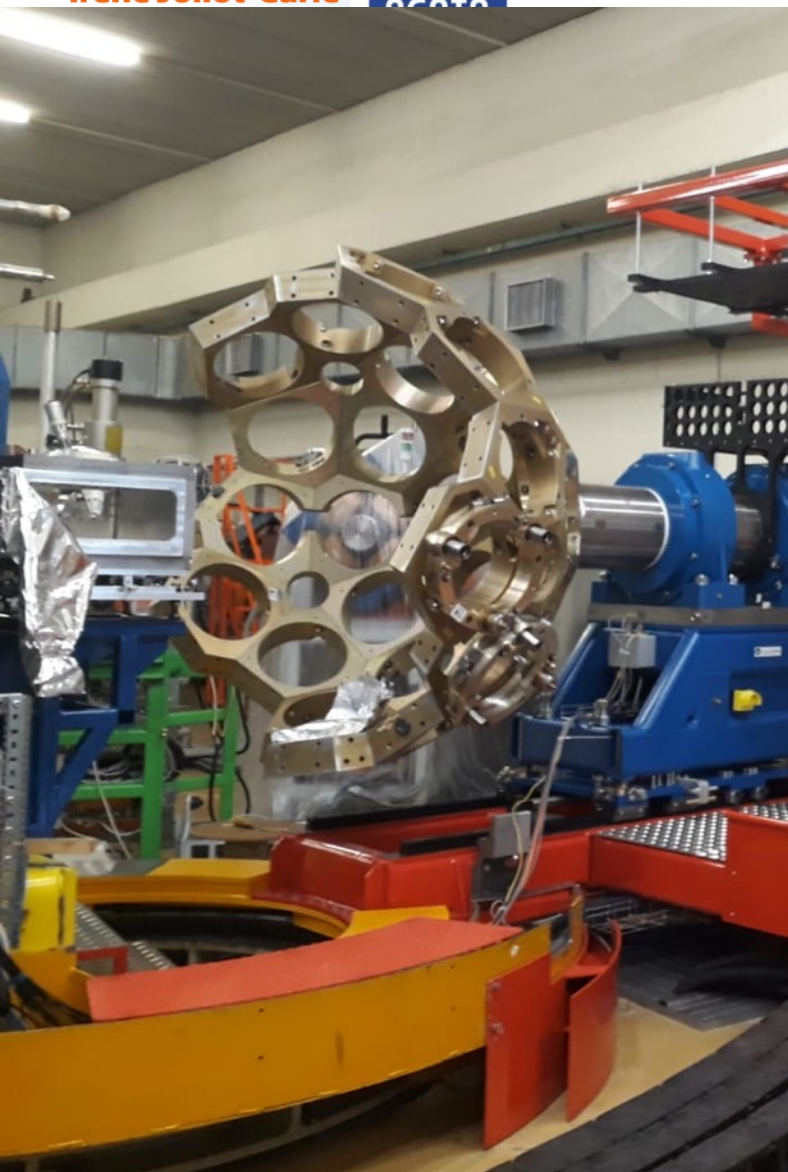
- Signal integrity is extremely important in AGATA (Energy resolution)
- At least 2 EMC qualifications for each phase
- Tests of all the mechanical and electrical connections
- 3 Visits @LNL . July 2021, November 2021 and April 2022.
- More 150 test points per test
- 3 types of tests (4Wire resistance, Direct and indirect injection).
- First visit in July gave successful results.



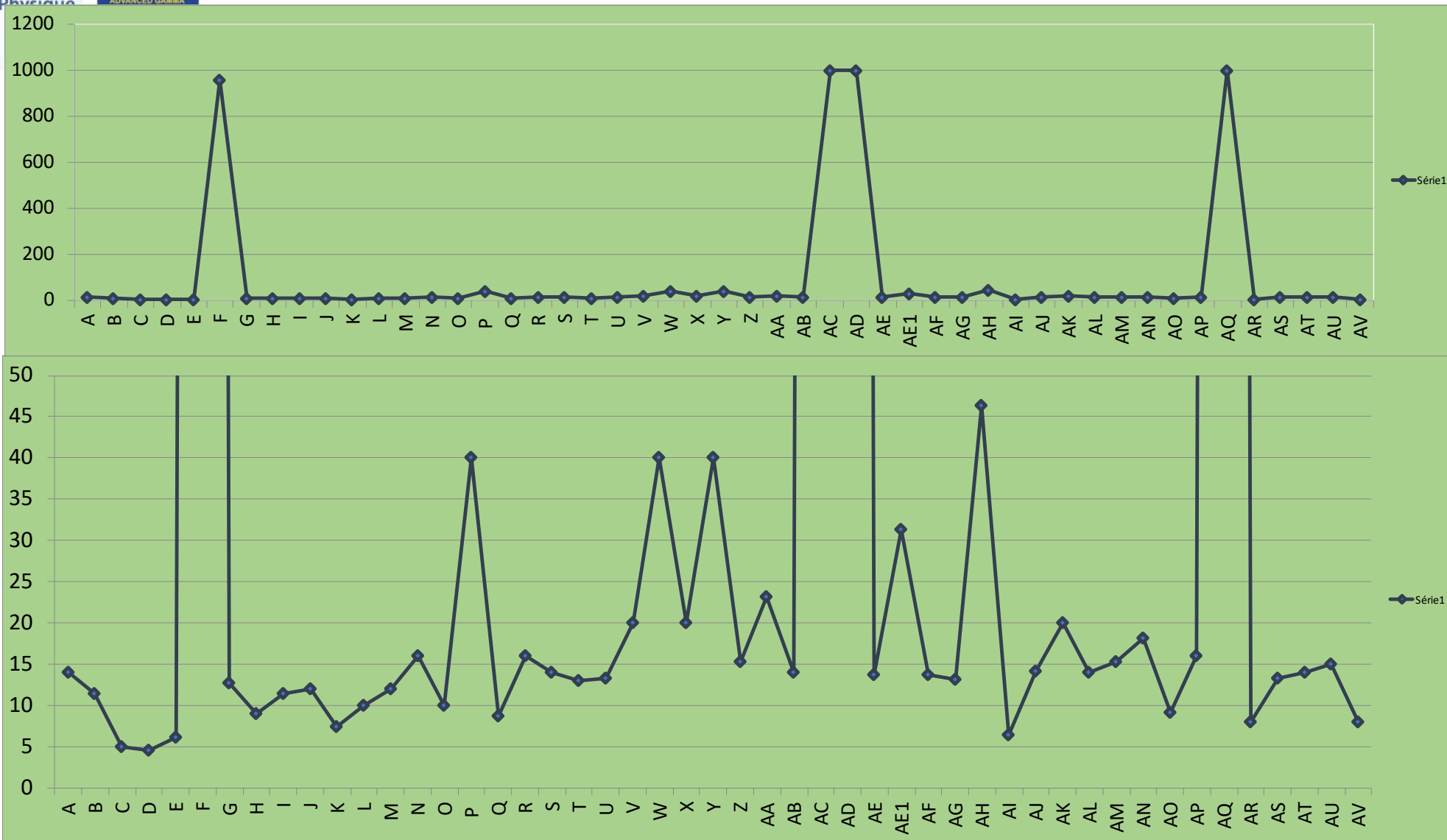
Direct injection to the ground using 4 kV pulser

4 wire resistance measurements (mOhms)



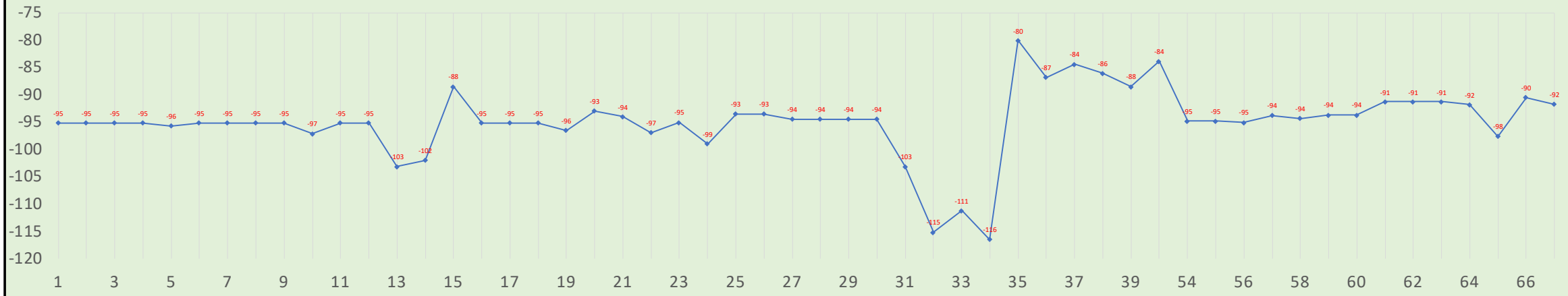


4 wire resistance measurements (mOhms)

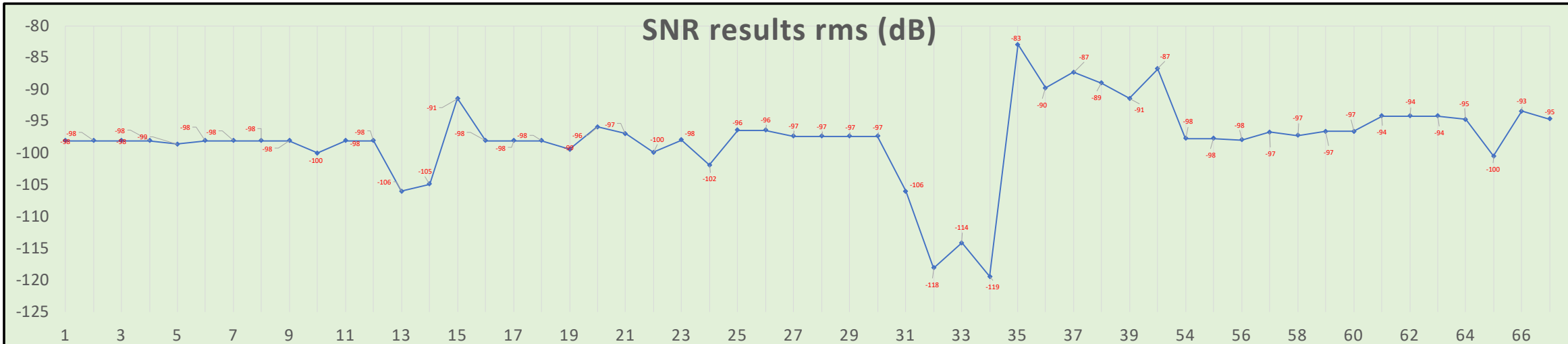


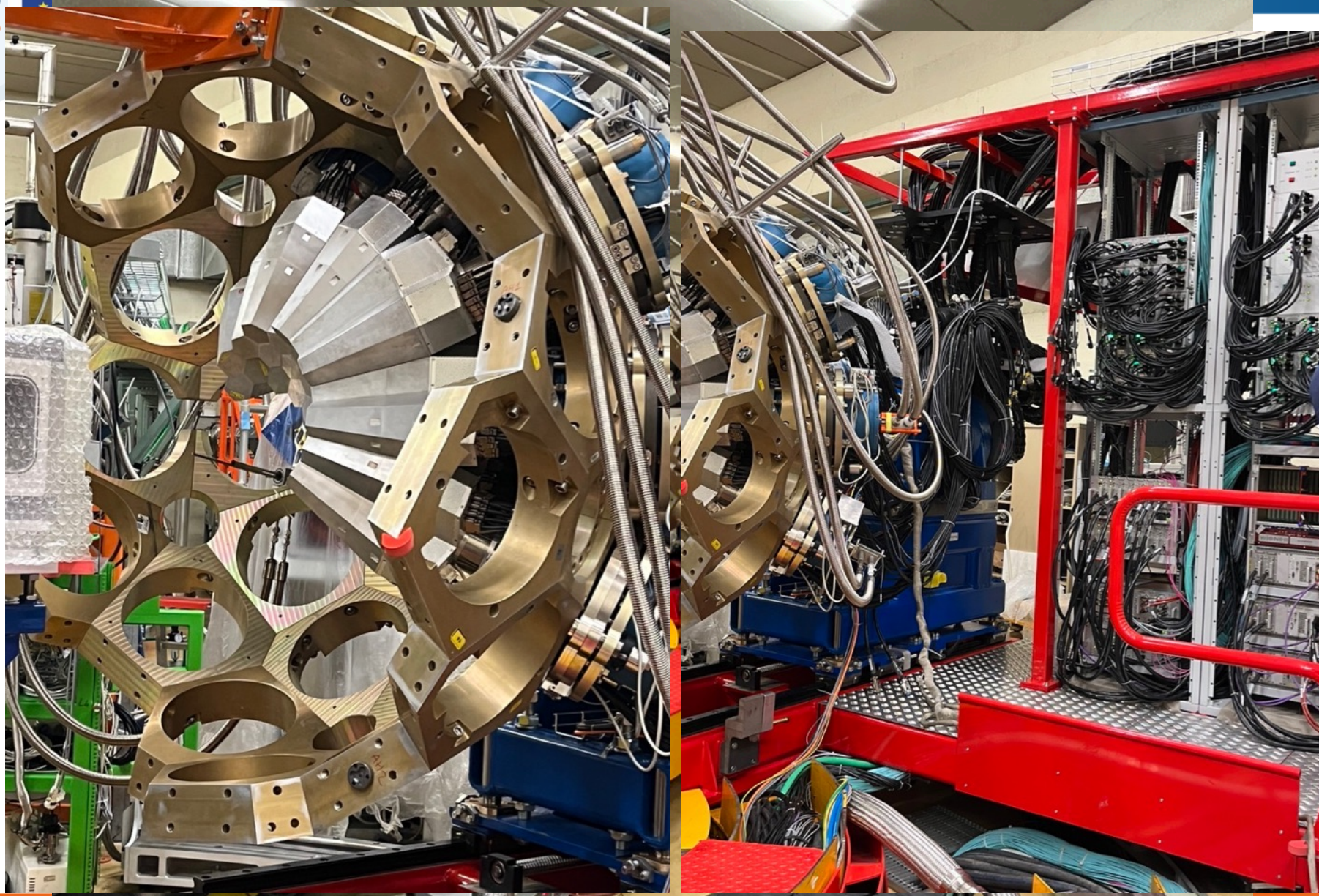
Direct injection to the ground using 4 kV pulser

SNR results p-p (dB)

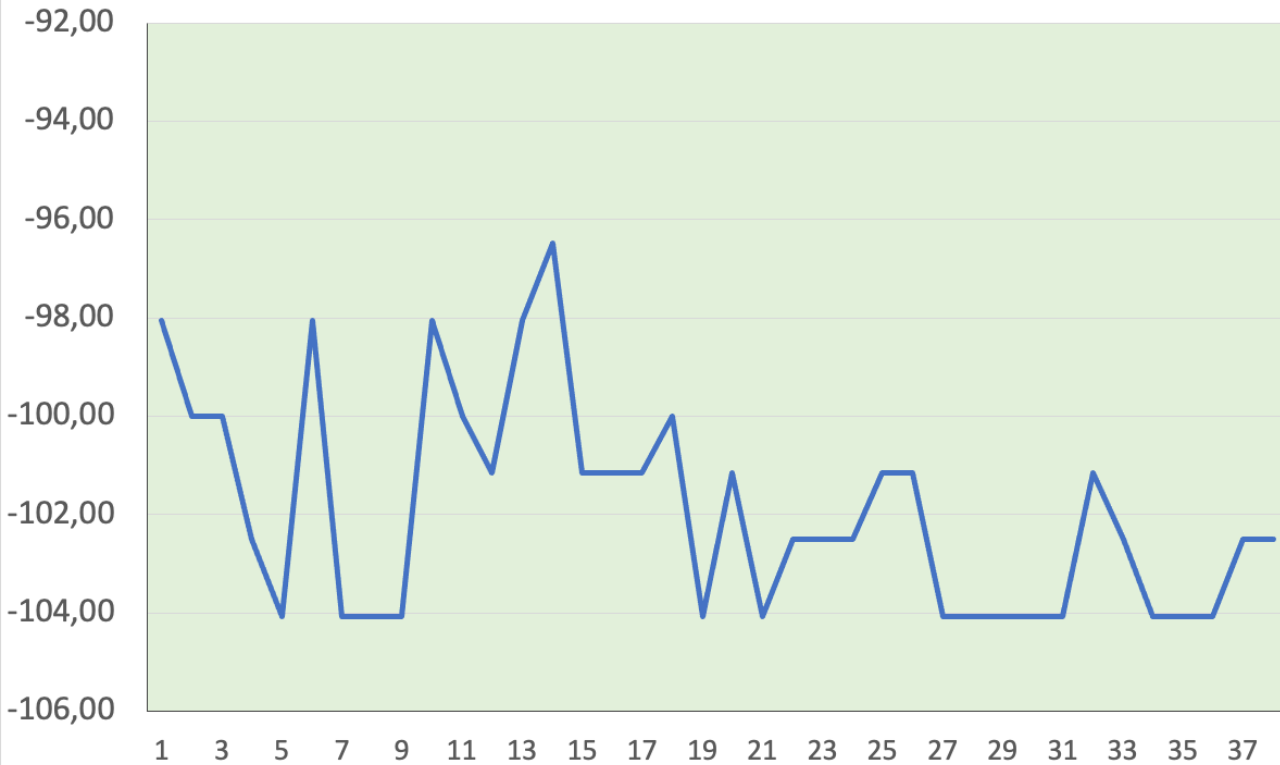


SNR results rms (dB)

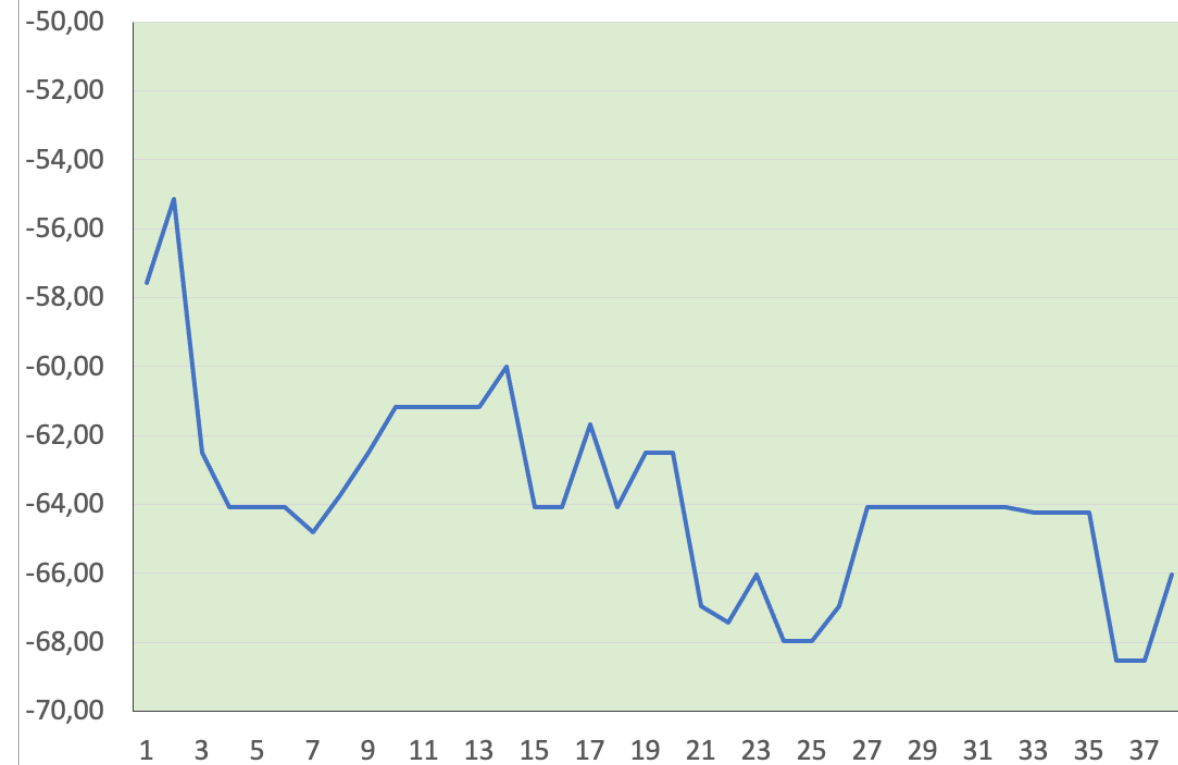




10-B (Direct injection on preamplifier output @ 4 kV, results in dB)

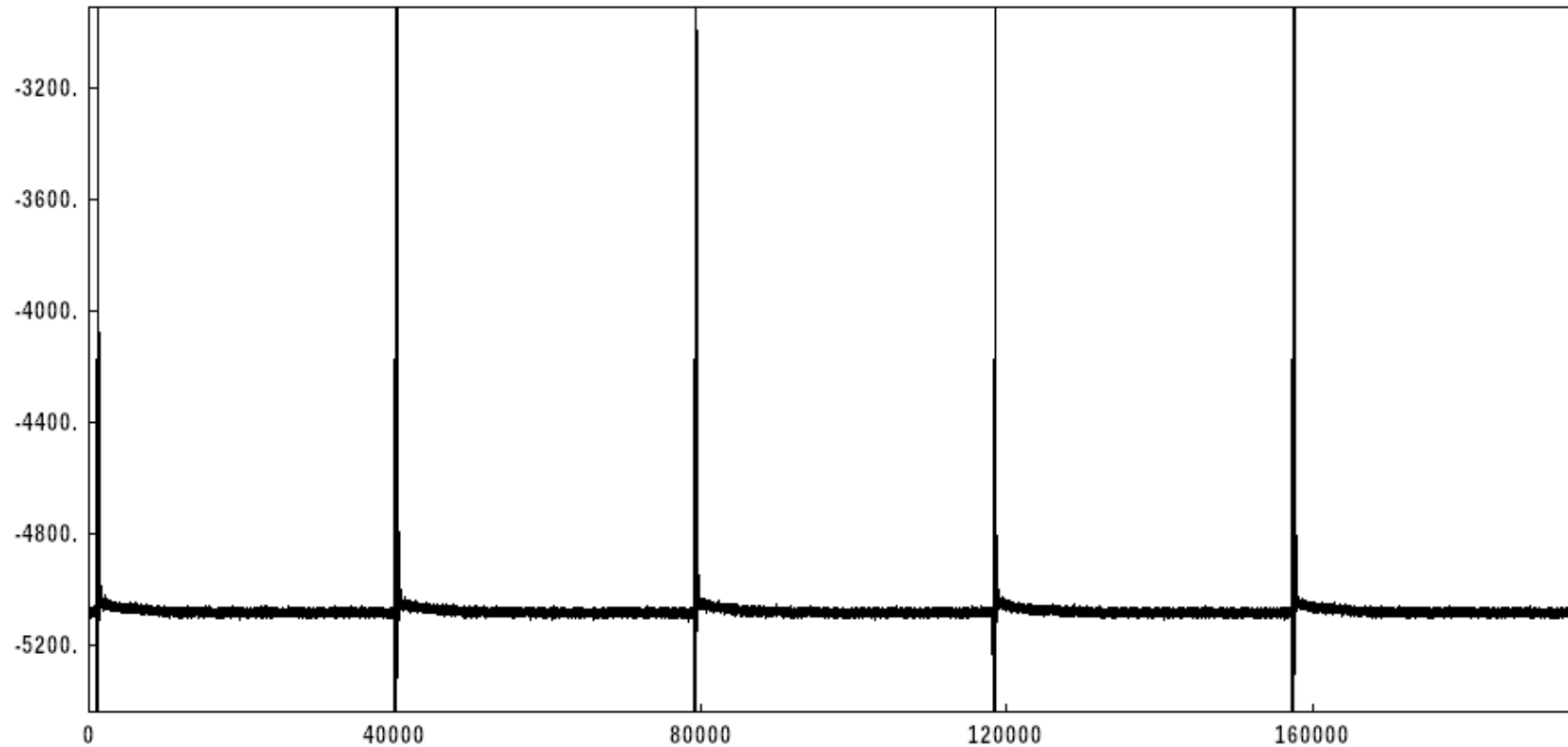


10-B (capacitive injection on preamplifier output @ 4 kV, results in dB)



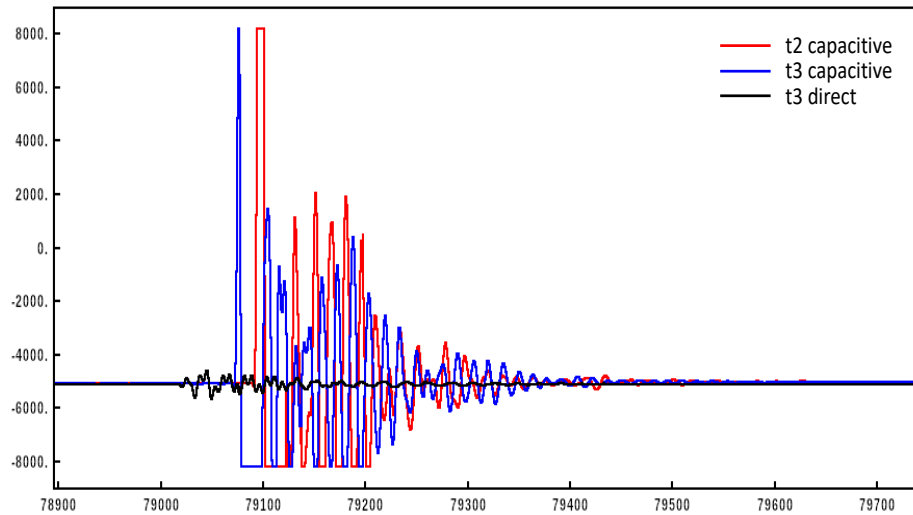
Detector 01A traces

Capacitive injection noise effect

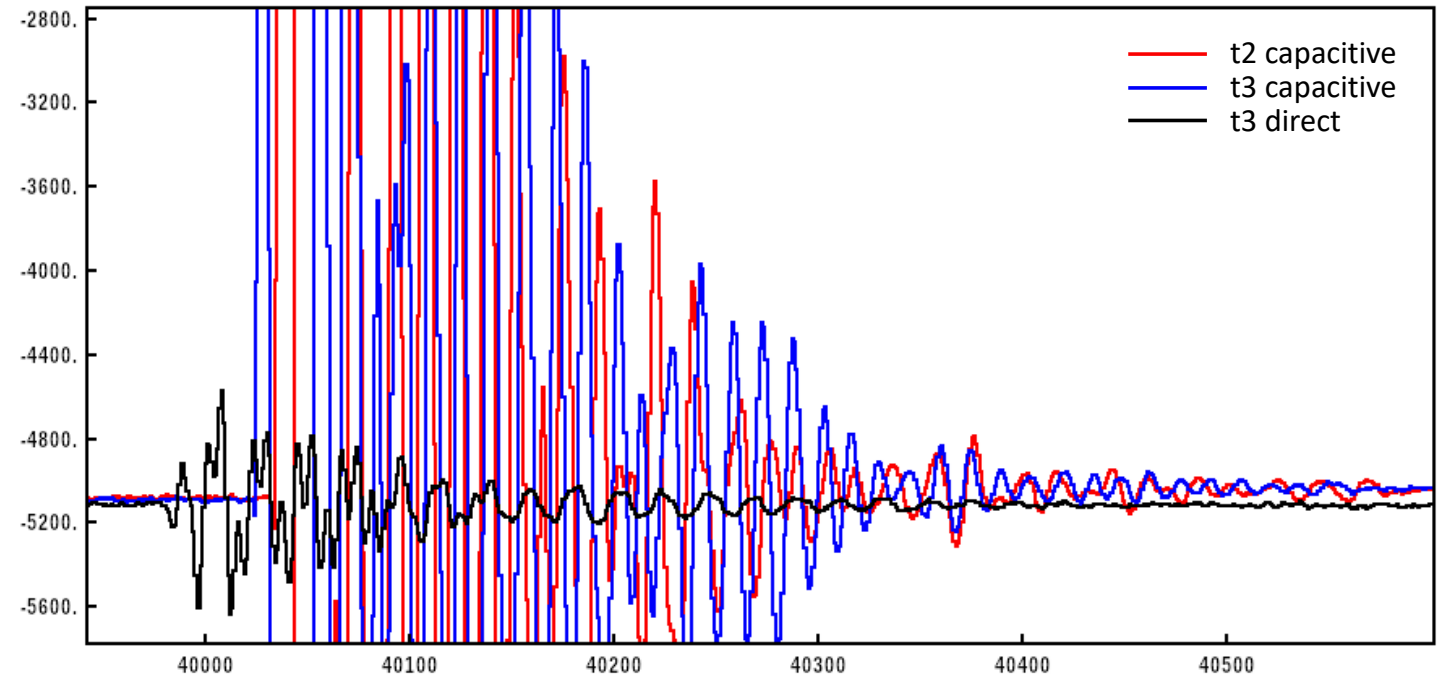


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Detector 01A traces



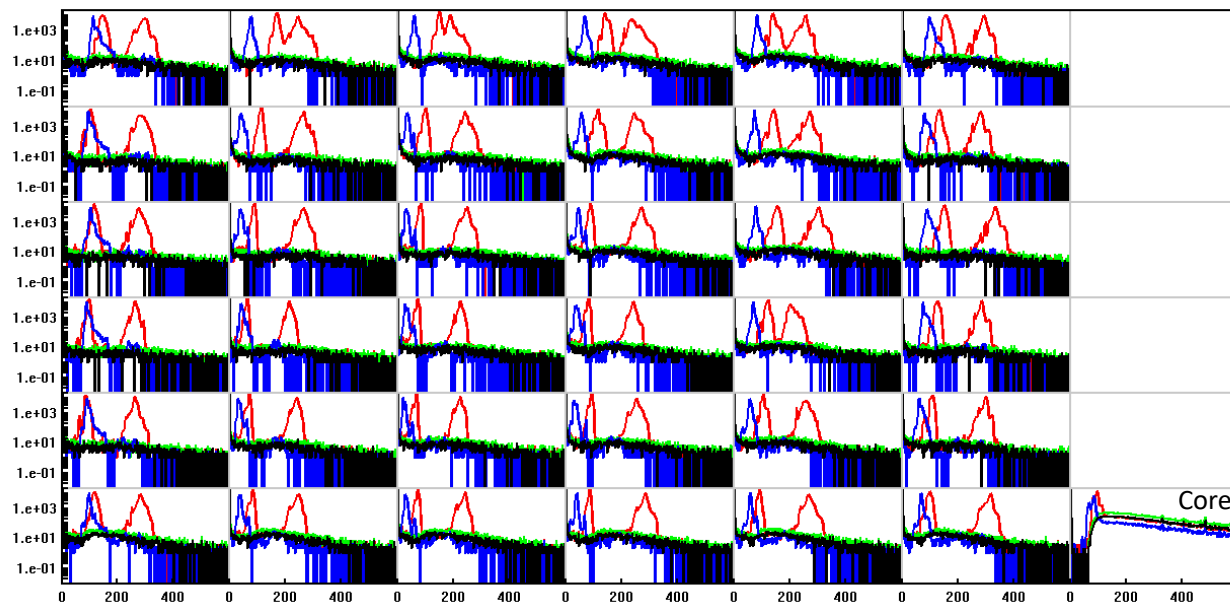
Detector 01A traces



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Detector 01A energy spectra

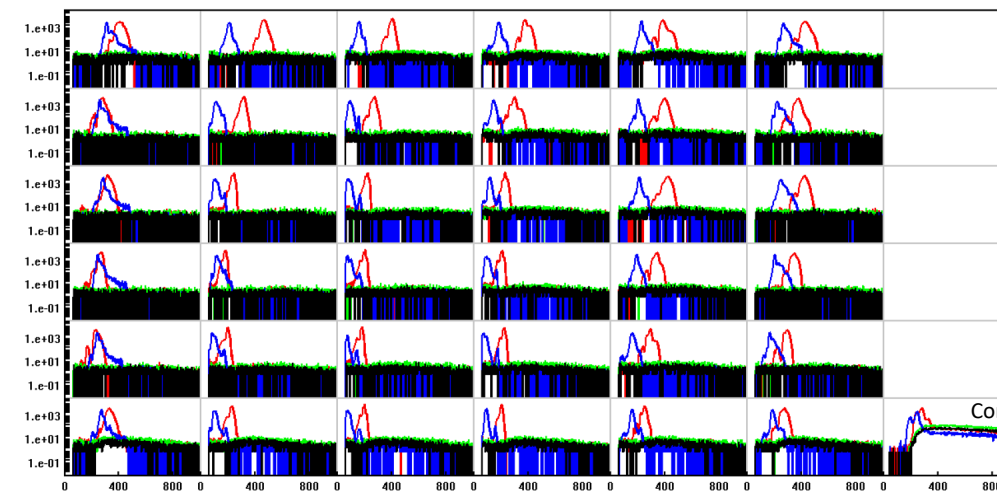
- t1 without noise
- t2 capacitive
- t3 capacitive
- t3 direct



Amplitud energy spectra

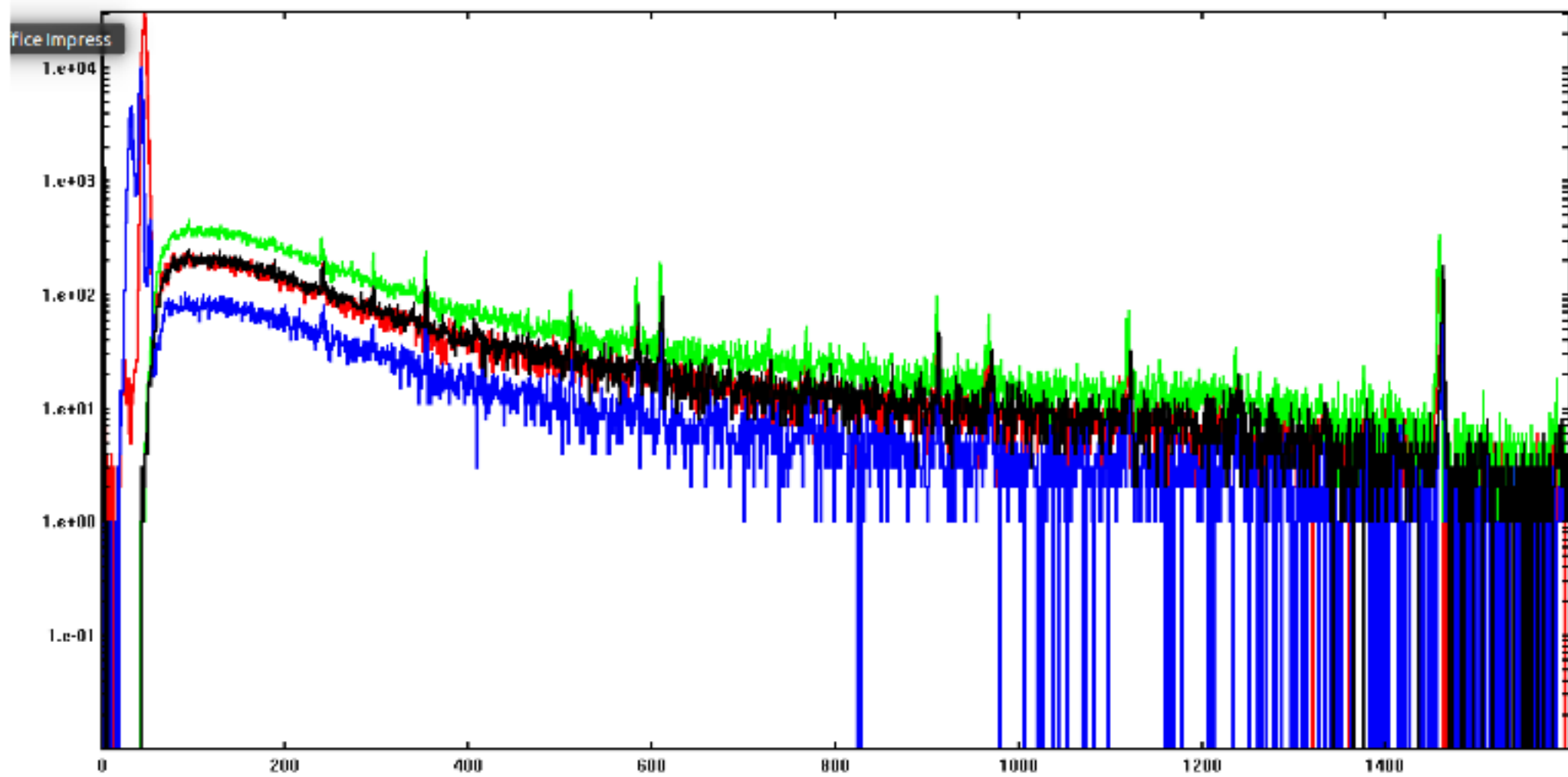
Detector 01A energy spectra

- t1 without noise
- t2 capacitive
- t3 capacitive
- t3 direct



Calibrated energy spectra

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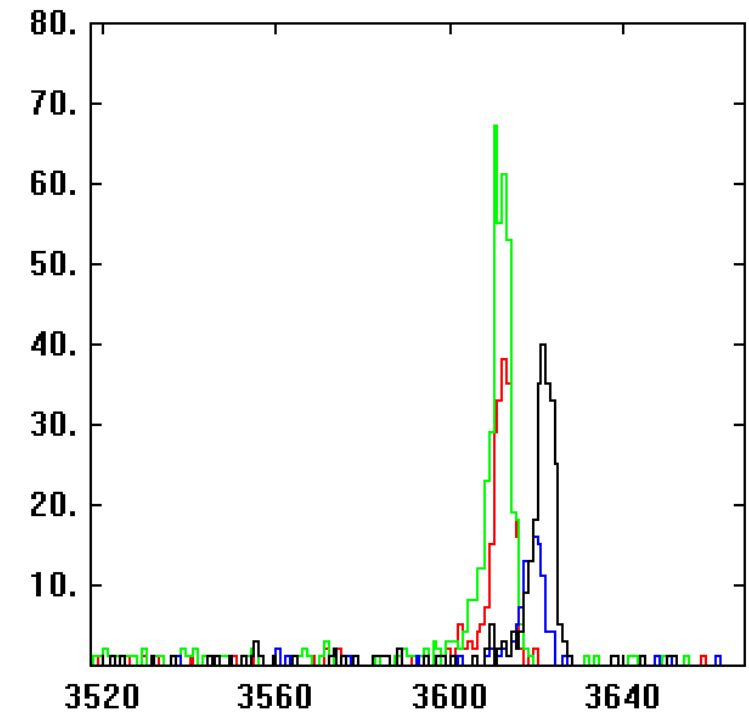
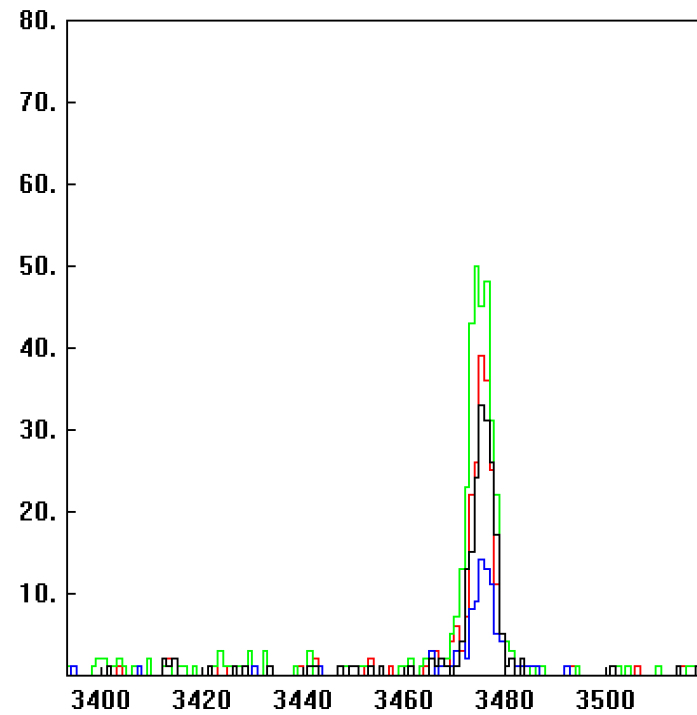
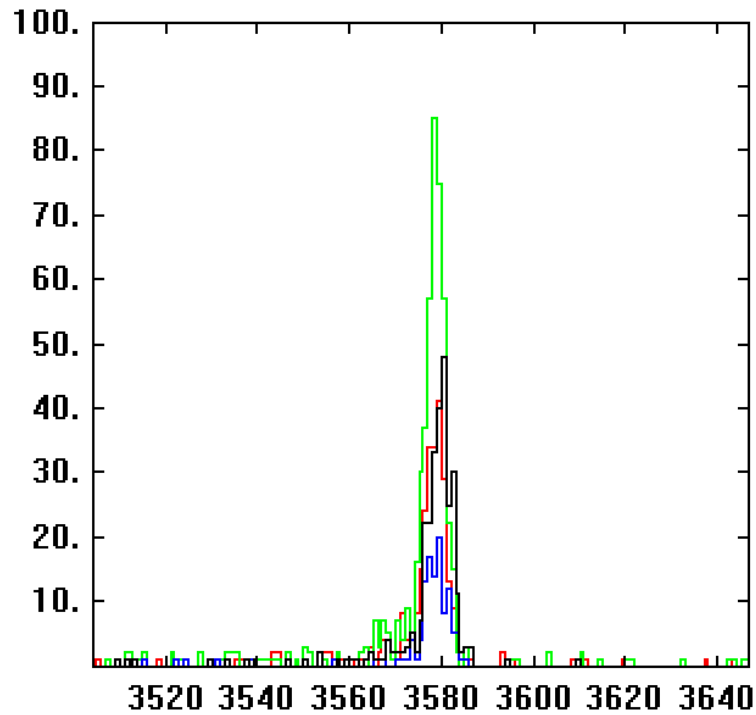


- t1 without noise
- t2 capacitive
- t3 capacitive
- t4 direct

01A

01B

01C



Amplitud energy spectra

Merci